

**IN THE SPECIFICATION:**

This application is a continuation of U.S. patent application Serial No. 10/126,600, entitled "LOW LOSS PRINTED CIRCUIT BOARD ANTENNA STRUCTURE AND METHOD OF MANUFACTURE THEREOF", filed on April 19, 2002. The above-listed application is commonly assigned with the present invention and is incorporated herein by reference as if reproduced herein in its entirety.

**IN THE CLAIMS:**

Claims 1-20 are presently canceled without prejudice or disclaimer.

21. (New) An antenna structure, comprising:

an antenna trace formed on a substrate;

a ground plane formed on said substrate, wherein said ground plane is non-overlapping with said antenna trace; and

an insulation region extending through said substrate and located between said antenna trace and said ground plane.

22. (New) The antenna structure recited in Claim 21, wherein said ground plane is coplanar with said antenna trace.

23. (New) The antenna structure recited in Claim 21 wherein said insulation region includes a plurality of insulation regions.